

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2001	static with timing with (circuit design)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:18
L2	2641	static with timing same ((circuit design))	US-PGPUB; USPAT	OR	ON	2010/01/15 10:18
L3	1479	l2 and (mode corner)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:18
L4	194	l3 and mode and corner	US-PGPUB; USPAT	OR	ON	2010/01/15 10:19
L5	1467	l3 and (output result)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:19
L6	663	l5 and (static timing).ab.	US-PGPUB; USPAT	OR	ON	2010/01/15 10:19
L7	524	l6 and (multiple repeat\$4)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:19
L8	240	l7 and parallel	US-PGPUB; USPAT	OR	ON	2010/01/15 10:20
L9	31	(static near3 timing near3 analysis) and database and parameter and mode and (run same (series parallel))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L10	55	(static near3 timing near3 analysis) and low-level and high-level and (combin\$5 or merg\$3 or add\$2 or sum \$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L11	448	(static and timing).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L12	20	L11 and parameter and parallel\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L13	1305	synopsys.as.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L14	193	L13 and static and timing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L15	92	L14 and parameter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L16	21	L15 and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L17	1	("6289319").PN.	USPAT; USOCR	OR	OFF	2010/01/15 10:22
L18	2	("6289319").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L19	2	("6298319").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L20	0	("(staticnear3timingnear3analysis)and (minimumsamemaximumsametime)").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L21	368	(static near3 timing near3 analysis) and (minimum same maximum same time)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L22	253	L21 and parameter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L23	154	L22 and run	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L24	64	L22 and timing same report	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L25	366	((static near3 timing near3 analysis) and circuit).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L26	4	L25 and what\$if	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L27	71	L25 and ((store\$2 sav\$3 display\$3) same result)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L28	778	703/19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L57	54	(static same timing same analysis) and coverage and (parallel OR simultan\$7) and corner and path	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L58	4	(static and timing).ab. and coverage and (parallel OR simultan\$7) and corner and path	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L59	6	(static and timing).ab. and coverage and corner and path	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L61	0	(static with timing) and simulation and model and corner and (percentage same timing same violation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L62	0	(static with timing) and simulation and model and corner and (percent same timing same violation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L63	9	(static with timing) and simulation and model and corner and percent	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L64	8	(static with timing) and simulation and model and corner and (timing same stability) and circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L65	1	(static with timing) and simulation and mode and corner and (timing same stability) and circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L67	417	(static with timing with analysis) and (stability reliability accuracy) and circuit and simulation and model	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L68	84	L67 and corner	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L69	31	L68 and percent\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L72	7	(timing same error same percentage) and (static with timing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L74	0	(static with timing) and simulation and model and corner and (percent same timing same error)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L75	5	(static with timing) and simulation and model and corner and (percent\$3 same timing same error)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L76	0	(static with timing with analysis) and (failure with rate) and corner	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L77	21	(static with timing with analysis) and (failure with rate) and path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L78	472	(static with timing with analysis).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L79	31	L78 and mode and corner	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L80	1815	(static adj timing) with (analy\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L81	239	L80 and corner	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L82	33	L81 and coverage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L83	2219	(static adj timing)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L84	41	L83 and corner and coverage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L85	0	L84 not L83	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L86	8	L84 not L82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L91	104	(static adj timing) and corner and node and (circuit IC)	US-PGPUB; USPAT; USOCR	OR	OFF	2010/01/15 10:22
L94	583	(static adj timing).ab.	US-PGPUB; USPAT; USOCR; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L95	53	L94 and corner	US-PGPUB; USPAT; USOCR; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L96	67	L94 and corner	US-PGPUB; USPAT; USOCR; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22

L97	65	L96 and (circuit IC)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L98	58	L97 not L82 not L84	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L99	10	schultz.in. and (static with timing)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L100	6	L99 and corner	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/01/15 10:22
L102	14	(static same circuit same timing same analysis) and unprocessed	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L103	4	(static same circuit same timing same analysis) and unanalyzed	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22

L104	4	(static same timing same analysis) and unanalyzed	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/01/15 10:22
L105	11050	((circuit IC) with timing).ab.	US-PGPUB; USPAT	OR	OFF	2010/01/15 10:26
L106	2001	I1 and static	US-PGPUB; USPAT	OR	OFF	2010/01/15 10:26
L107	96	I106 and mode and corner	US-PGPUB; USPAT	OR	OFF	2010/01/15 10:26
L108	134	I106 and mode and corner	US-PGPUB; USPAT	OR	ON	2010/01/15 10:26
L109	1472	((circuit IC) with static with timing)	US-PGPUB; USPAT	OR	OFF	2010/01/15 10:27
L110	102	I109 and mode and corner	US-PGPUB; USPAT	OR	ON	2010/01/15 10:27
L111	774	I109 and (mode corner) and ((timing) with result output report)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:27
L112	412	I111 and (static timing).ab.	US-PGPUB; USPAT	OR	ON	2010/01/15 10:29
L113	5	I112 and (no\$2examin\$4 no\$2analyzed unexamin\$4 unanalyzed)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:30
L114	35	I112 and coverage	US-PGPUB; USPAT	OR	ON	2010/01/15 10:30
L115	2	I109 and (mode corner) and (percent\$4 with violat \$4)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:31
L116	6	I109 and (percent\$4 with violat\$4)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:31
L117	331	I109 and violat\$4	US-PGPUB; USPAT	OR	ON	2010/01/15 10:31

L118	17457	702/57 702/58 702/79 702/89 702/125 703/3 703/4 703/13 703/14 703/19 714/55 714/814 714/815 716/4 716/5 716/6	US-PGPUB; USPAT	OR	ON	2010/01/15 10:37
L119	14195	1118 and (circuit IC)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:37
L120	2110	1119 and timing and static	US-PGPUB; USPAT	OR	ON	2010/01/15 10:37
L121	1183	1120 and (mode corner)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:38
L122	182	1121 and coverage	US-PGPUB; USPAT	OR	ON	2010/01/15 10:38
L123	165	1121 and (mode and corner)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:38
L124	237	1121 and violation	US-PGPUB; USPAT	OR	ON	2010/01/15 10:38
L125	1126	1121 and (ouput result)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:38
L126	1135	1121 and (ouput result report)	US-PGPUB; USPAT	OR	ON	2010/01/15 10:39
L127	182	1126 and (mode corner) and coverage	US-PGPUB; USPAT	OR	ON	2010/01/15 10:39
L135	1101	(static and timing and (circuit IC)).clm.	US-PGPUB; USPAT	OR	ON	2010/01/15 10:40
L136	220	1135 and (mode corner).clm.	US-PGPUB; USPAT	OR	ON	2010/01/15 10:40
L137	46	1135 and violation.clm.	US-PGPUB; USPAT	OR	ON	2010/01/15 10:40

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
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L128	15545	702/57 702/58 702/79 702/89 702/125 703/3 703/4 703/13 703/14 703/19 714/55 714/814 714/815 716/4 716/5 716/6	USPAT; UPAD	OR	ON	2010/01/15 10:39
L129	1821	I128 and static and timing and (circuit IC)	USPAT; UPAD	OR	ON	2010/01/15 10:39
L130	44	I129 and (mode corner) and coverage and violation	USPAT; UPAD	OR	ON	2010/01/15 10:39
L131	734	(static and timing and (circuit IC)).clm.	USPAT; UPAD	OR	ON	2010/01/15 10:39
L132	0	I131 and ((mode corner) and coverage and violation).clm.	USPAT; UPAD	OR	ON	2010/01/15 10:40
L133	0	I131 and (coverage and violation).clm.	USPAT; UPAD	OR	ON	2010/01/15 10:40
L134	2	I131 and (coverage).clm.	USPAT; UPAD	OR	ON	2010/01/15 10:40

1/15/2010 10:42:18 AM

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